

AMENDMENT TO CLAIMS

In The Claims:

Please amend the claims as follows:

1. - 24. (Canceled)

28. (Currently Amended) A thin film transistor array panel, comprising:
a plurality of pixels defined by gate lines and data lines; and
a plurality of thin film transistors and pixel electrodes formed at the pixel and electrically connected to the gate lines and the data lines,
wherein each of the plurality of the thin film transistors ~~[[have]]~~ has a semiconductor layer and an ohmic layer, said semiconductor layer having a double-layered structure with a lower layer and an upper layer, each made of amorphous silicon ~~layers, which,~~
wherein the lower layer and the upper layer each have different band gaps.

29. (Previously Presented) The thin film transistor of claim 28, wherein the upper amorphous silicon layer is formed on the lower amorphous silicon layer, wherein the band gap of the upper amorphous silicon layer is less than that of the lower amorphous silicon layer.

30. - 35. (Canceled)

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